FY2018 1Q
(Three months ended June 30th, 2018)
Information Meeting

July 25th, 2018
Advantest Corporation
Agenda

- Financial Results for FY2018 1Q
  Atsushi Fujita
  Managing Executive Officer

- FY2018 Business Outlook
  Yoshiaki Yoshida
  Representative Director, President & CEO
### FY18 1Q Summary of Results

<table>
<thead>
<tr>
<th>FY17</th>
<th>FY18</th>
<th>QoQ</th>
<th>YoY</th>
</tr>
</thead>
<tbody>
<tr>
<td>1Q</td>
<td>2Q</td>
<td>3Q</td>
<td>4Q</td>
</tr>
<tr>
<td>Orders</td>
<td>49.4</td>
<td>56.2</td>
<td>56.8</td>
</tr>
<tr>
<td>Sales</td>
<td>40.7</td>
<td>47.7</td>
<td>50.9</td>
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<tr>
<td>Gross Profit</td>
<td>20.6</td>
<td>24.9</td>
<td>23.2</td>
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<tr>
<td>Gross Profit Margin</td>
<td>50.6%</td>
<td>52.1%</td>
<td>45.8%</td>
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<tr>
<td>Operating Income</td>
<td>2.2</td>
<td>5.2</td>
<td>3.2</td>
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<tr>
<td>Operating Income Margin</td>
<td>5.5%</td>
<td>10.9%</td>
<td>6.4%</td>
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<tr>
<td>Income Before Tax</td>
<td>1.6</td>
<td>4.8</td>
<td>3.4</td>
</tr>
<tr>
<td>Net Income</td>
<td>1.0</td>
<td>3.9</td>
<td>2.6</td>
</tr>
<tr>
<td>Net Income Margin</td>
<td>2.4%</td>
<td>8.2%</td>
<td>5.0%</td>
</tr>
</tbody>
</table>

Adjusted initial backlog following implementation of IFRS15 ▲3.0

Backlog 50.9 | 59.4 | 65.3 | 82.8 | 79.5 | ▲1.3 | ▲4.0% | 28.6 | 56.2%

Exchange Rate

<table>
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<tr>
<th>1 US$</th>
<th>¥112</th>
<th>¥111</th>
<th>¥112</th>
<th>¥111</th>
<th>¥108</th>
<th>¥3 Appreciation</th>
<th>¥4 Appreciation</th>
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</thead>
<tbody>
<tr>
<td>1 Euro</td>
<td>¥121</td>
<td>¥128</td>
<td>¥132</td>
<td>¥134</td>
<td>¥131</td>
<td>¥3 Appreciation</td>
<td>¥10 Depreciation</td>
</tr>
</tbody>
</table>

### FY2018 1Q Results Summary

- In 1Q, the semiconductor market continued to expand across a wide range of fields, and performance improvement trends, especially in high-end products, also progressed rapidly. Against this backdrop, semiconductor manufacturers continued to actively invest in testers, creating a favorable business environment for Advantest.

- Our strength is our customer base. We continued to receive many orders from a wide range of customers in 1Q.

- We achieved ¥70.6 billion in orders. Although this is a decrease of ¥14.8 billion QoQ, the result is very strong. Excluding the previous quarter, this is the first time since FY2005 3Q that we have surpassed ¥70 billion.

- Sales was ¥70.9 billion, increasing for a fifth consecutive quarter.

- Gross margin was 53.9%. While this was a 1.8% decrease QoQ, it remains strong.

- We controlled SG&A to achieve ¥15.8 billion in operating income. The operating margin exceeded 20% for a second consecutive quarter.

- Our income before tax was ¥16.5 billion, net income was ¥13.9 billion, and net income margin was about 20%.

- Advantest adopted IFRS15 “Revenue from Contracts with Customers” in FY2018.

- Adjustments were applied at the beginning of the fiscal year.

- The impact on our backlog was -3.0 billion yen. As a result, the backlog as of the end of 1Q is ¥79.5 billion.

- Foreign exchange rates were ¥108 to the US dollar and ¥131 to the Euro.
Business Domains Renamed

- Starting in FY18, Advantest has renamed our business domains

  Non-Memory Semiconductor Test Systems (Non-Memory Testers) → SoC Test Systems (SoC Testers)

  Memory Semiconductor Test Systems (Memory Testers) → Memory Test Systems (Memory Testers)

Note: The definitions of these markets remain unchanged

Business Domains Renamed

- Until now, we roughly divided our semiconductor test equipment business into two domains based on the type of device supported: non-memory and memory.
- We renamed these domains in FY2018: SoC and memory.
- There is no change to the target markets for our SoC tester products and memory tester products respectively.
- In the narrow sense, SoC refers to “System on Chip” devices integrating various types of IP, but we will use it as a synonym for “non-memory.”
- In Advantest’s usage, SoC is more broadly defined to include logic ICs such as MPUs and GPUs, communication devices such as baseband processors, MCUs, analog ICs, sensors, optical devices, display drivers, etc.
- Memory testers specialize in testing DRAM and non-volatile memory such as NAND.
FY2018 1Q Orders by Segment

Semiconductor & Component Test Systems
-  ¥53.6 billion (-11% QoQ)
  - of which, SoC testers ¥41.8 billion
  - memory testers ¥11.8 billion
- SoC testers posted record high quarterly orders.
- The background to this achievement is the qualitative and quantitative changes in the semiconductor market.
- Particularly in application processors, which are core components of smartphones, miniaturization and incorporation of AI chips is driving performance evolution. This leads to enhanced test complexity and longer test times, boosting demand for testers.
- Orders for SoC testers were also driven by customer investment in a wide range of areas including automotive, industrial equipment, and consumer products.
- Memory tester orders appear to have shrunk due to a large number of advance orders posted in the previous quarter. There was no sense of deceleration in reality, and we received solid orders for both DRAM and non-volatile memory.

Mechatronics Systems
-  ¥9.4 billion (-34% QoQ)
  - Device interface orders fell in step with memory testers, and nanotechnology product enquiries were sluggish.

Services, Support & Others
-  ¥7.7 billion (-31% QoQ)
  - As many maintenance contracts were renewed at the end of the previous fiscal year, this quarter saw a decline in contrast.
### FY2018 1Q Orders by Region

- **Regions posting significant increases / decreases QoQ**
  - **Taiwan**: up ¥9.8 billion to ¥33.9 billion
    
    Test demand for application processors used in smartphones increased.
  - **South Korea**: down ¥17.1 billion to ¥15.2 billion
    
    There was a decline in memory tester orders in reaction to the large number of advance orders received in the previous quarter.
FY2018 1Q Sales by Segment

- While the trend is for 1Q sales to fall below the previous quarter, this 1Q showed great strength as sales increased QoQ.

Semiconductor & Component Test Systems

- ¥51.0 billion +3% QoQ
  - of which, SoC testers ¥31.6 billion
  - memory testers ¥19.4 billion

- SoC testers maintained the high level of sales of the previous quarter, principally in the application processor and high-end logic IC segments.

- Memory tester sales increased overall. In addition to continued high DRAM tester sales, sales of non-volatile memory testers increased even further from the previous quarter.

Mechatronics Systems

- ¥11.1 billion +11% QoQ

Services, Support, & Others

- ¥8.9 billion +5% QoQ
O FY2018 1Q Sales by Region

- Sales were high, primarily in Asia, where semiconductor volume production processes are concentrated.
- As a result, Advantest's overseas sales ratio in 1Q was 93.5%.
### FY2018 1Q Sales / Gross Profit / Operating Income

- **Gross Margin**: -1.8 points QoQ 53.9%
  While there was a change in product mix, we continued to maintain a high level.

- **SG&A**: - ¥1.6 billion QoQ ¥22.4 billion
  Administrative expenses such as R&D expenses were low because this is the beginning of the fiscal year.

- **Operating Income**: +¥2.0 billion QoQ ¥15.8 billion

- **Operating Margin**: +1.9 points QoQ 22.3%
  Decrease in SG&A compensated for the decline in gross margin.
○ FY2018 1Q R&D Expenses/Capex/Depreciation and Amortization

- R&D Expenses  ¥8.6 billion
  
  R&D to sales ratio  12.2%

- Capex  ¥1.2 billion

- Depreciation and Amortization  ¥1.2 billion
Balance Sheet for Period Ending June 30th, 2018

- Total Assets: ¥271.8 billion
- Cash and cash equivalents: ¥106.4 billion
  - ¥2.5 billion increase from the end of the previous fiscal year
- Bonds, Current Liabilities: down ¥5.3 billion to ¥24.7 billion
  - Conversion of convertible bonds maturing in February 2019 progressed, and the conversion rate as of the end of June was 17%.
- Equity Attributable to Owners of the Parent: ¥142.5 billion
- Ratio of Equity Attributable to Owners of the Parent: 52.4%
  - Increased by 3.4 points from the end of the previous fiscal year
Agenda

- Financial Results for FY2018 1Q
  Atsushi Fujita
  Managing Executive Officer

- FY2018 Business Outlook
  Yoshiaki Yoshida
  Representative Director, President & CEO
CY 2018 Market Forecast

- Firstly, regarding the tester market, which is the foundation of our business, our figures from three months ago remain unchanged.

- Half of the calendar year is over, and semiconductor demand is steady. We estimate that there will be no significant change in customer investment schedules.

- While the media has shown concern for modulation in semiconductor equipment investment, we believe that tester investment as originally anticipated will materialize within the year.

- The SoC tester market in the first half of the calendar year saw booming demand for testers for high-end logic ICs used in AI servers and testers for smartphone processors.

- SoC tester demand typically declines somewhat in the second half of the calendar year. We expect to see the same seasonal trend this year, but SoC tester investment is expected to hold steady in areas such as smartphones, industrial equipment, automotive, and display-related devices.

- Our outlook for memory manufacturers’ investment ranges and schedules is not greatly different from what it was three months ago. Demand for premium DRAM and non-volatile memory used in servers is solid, and we expect investment in testers will be carried out accordingly.

- Although we cannot yet predict the size of the tester market in 2019, we expect the 5G smartphone market to start ramping up.

- Regarding 5G smartphones, baseband processors and RF components that manage communication processing will be enhanced.

- The impact of 5G also goes beyond that. In order to handle massive high-speed data processing and data generation inside the smartphone, all semiconductors used in 5G smartphones will need to cope with higher speeds and shorter latencies and there will need to be more semiconductors in each device.

- In addition to SoC semiconductors, memory device will also need to have greater capacities and higher speeds.

- We see the possibility of a new wave of demand in the tester market from 2019 to 2020.

### CY18 Market Forecast

<table>
<thead>
<tr>
<th></th>
<th>CY17</th>
<th>CY18 estimate</th>
<th>YoY Change</th>
</tr>
</thead>
<tbody>
<tr>
<td>SoC tester market</td>
<td>Approx. $2,200M</td>
<td>Approx. $2,200~$2,300M</td>
<td>+0%~5%</td>
</tr>
<tr>
<td>Memory tester market</td>
<td>Approx. $750M</td>
<td>Approx. $900M</td>
<td>Approx. +20%</td>
</tr>
</tbody>
</table>

- We maintain our market outlook of 3 months ago. We expect tester investment development in line with our existing outlook
- SoC tester demand in CY2H will center on smartphones and display-related devices
- Our outlook for memory tester demand expansion remains unchanged
FY18 Forecast

<table>
<thead>
<tr>
<th></th>
<th>FY17</th>
<th>FY18 Forecast</th>
<th>vs. FY17</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>(¥B)</td>
<td>(¥B)</td>
<td>(Δ)</td>
</tr>
<tr>
<td>Orders</td>
<td>247.8</td>
<td>220.0</td>
<td>▲27.8</td>
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<tr>
<td>Sales</td>
<td>207.2</td>
<td>230.0</td>
<td>22.8</td>
</tr>
<tr>
<td>Semiconductor &amp; Component Test systems</td>
<td>140.9</td>
<td>160.0</td>
<td>19.1</td>
</tr>
<tr>
<td>Mechatronics Systems</td>
<td>35.9</td>
<td>38.0</td>
<td>2.1</td>
</tr>
<tr>
<td>Services, Support &amp; Others</td>
<td>30.5</td>
<td>32.0</td>
<td>1.5</td>
</tr>
<tr>
<td>Operating Income</td>
<td>24.5</td>
<td>34.5</td>
<td>10.0</td>
</tr>
<tr>
<td>Operating Income Margin</td>
<td>11.8%</td>
<td>15.0%</td>
<td>3.2pts</td>
</tr>
<tr>
<td>Income Before Tax</td>
<td>24.3</td>
<td>34.8</td>
<td>10.5</td>
</tr>
<tr>
<td>Net Income</td>
<td>18.1</td>
<td>27.8</td>
<td>9.7</td>
</tr>
<tr>
<td>Net Income Margin</td>
<td>8.7%</td>
<td>12.1%</td>
<td>3.4pts</td>
</tr>
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<td>Adjusted initial backlog following implementation of IFRS15</td>
<td>▲3.0</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Backlog</td>
<td>82.8</td>
<td>69.8</td>
<td>▲13.0</td>
</tr>
<tr>
<td>Exchange Rate</td>
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</tr>
<tr>
<td>1 US$</td>
<td>¥111</td>
<td>¥105</td>
<td>¥6 Appreciation</td>
</tr>
<tr>
<td>1 Euro</td>
<td>¥129</td>
<td>¥135</td>
<td>¥6 Depreciation</td>
</tr>
</tbody>
</table>

Note: Intersegment transactions have been eliminated from totals

FY2018 Forecast

- As stated in our new mid/long-term management policy announced in April, higher demand for semiconductors that require better performance and higher reliability, amid an overall expansion of the semiconductor market, presents an opportunity for Advantest.

- We believe that the progress of our orders and sales is a sign that the tester market is moving in line with our predictions under the mid/long-term management policy.

- In 1Q, we progressed towards our full-year forecast as follows:
  - Orders: 32.1%
  - Sales: 30.8%
  - Operating Income: 45.8%
  - Net Income: 50.0%

- If these trends are sustained, we believe our 2018 market share may improve considerably year-on-year.

- However, there are certain factors that could have a chilling effect on the global economy, such as trade friction between the US and China, so we need to keep an eye on these trends.

- For now, we are keeping our earnings forecasts unchanged.

- There is certainly some lack of visibility, but for the present our business environment is vibrant. If the world economy and the semiconductor industry do not undergo significant shocks in the future, we think it is possible that we achieve our current forecasts for the year.

- We will aim for further business growth with the goal of revising our forecasts upward.
FY18 Key Measures

**Towards Business Expansion**

*Reinforce product supply capability*
- We will strive to continue to ensure a stable parts supply that tracks tester demand

*Measures aimed at mid-term growth*
- In June 2018, we globally integrated our semiconductor test equipment business
- The new organization will enhance our competitiveness in promising areas such as AI, 5G comms and ADAS

**FY18 Investment Outlook: Unchanged from Initial Plan**

- R&D Expenses: ¥35.0 B (FY17 actual: ¥33.5 B)
- Capex: ¥7.0 B (FY17 actual: ¥5.4 B)
- Depreciation and amortization: ¥5.5 B (FY17 actual: ¥5.0 B)

○ FY2018 Key Measures

- Although our product supply capability has been improved considerably compared to one year ago, we are still in a situation where we cannot deliver products as fast as customers would wish.
- We will continue to make efforts to secure enough components.
- As a mid/long-term growth initiative, our businesses related to semiconductor testers were completely integrated after the general shareholders’ meeting in June, forming one global organization.
- Looking into the future, new devices going beyond conventional concepts, combining various technologies such as memory, SoC, AI and software will be launched. We will make a companywide effort in providing test technologies to complement such trends. By taking this bold move with our development resources around the globe, we have built a framework to provide new solutions. This framework will be the foundation of our growth over the next decade.
- Our investment outlook remains unchanged from our initial plan.
FY2018 Outlook by Segment: SoC Testers

- In 2018, semiconductors used in smartphones and AI servers are gaining higher performance and becoming more miniaturized, extending test times and making test more complicated. This is boosting demand for SoC testers.
- Although smartphone unit growth has slowed, handset performance is consistently improving in all segments, from high-end to low-end. Along with that, performance evolution of the semiconductors used in smartphones and increase in the number of semiconductors per device continues.
- To emphasize this point again, Advantest’s strength is our base of customers in every tier involved in the semiconductor market. Regarding smartphones, we are in a position to capture tester demand regardless of which products take off.
- This has led to record orders for SoC testers since the fourth quarter of the previous fiscal year, and will continue to deliver results in the future.
- The environment is also favorable for automotive and display-related semiconductors.
- Especially in the display-related segment, in addition to higher panel definition, testing is becoming more complicated as display driver ICs continue to incorporate touch sensors; these factors contribute to maintaining the momentum of the massive wave of demand.
Customers continue to invest in DRAM back-end testers to boost production capacity and support higher device speeds.

Higher flash memory stacks and increasing reliability needs will also drive NAND tester demand growth.

FY2018 Outlook by Segment: Memory Testers
- Sales prospects for memory testers remain unchanged.
- In DRAM, due to strong demand for server DRAM, demand for back-end DRAM testers is increasing and investment is expected to hold steady.
- Tester investment for volume production of 3D NAND for 64-layer and 72-layer flash, and investment in burn-in testers to ensure the reliability of NAND, are also expected to hold steady.
- Even if there should be adjustments in memory tester investment, we believe these adjustments will be short-term and growth will return in the medium to long term.
FY2018 Outlook by Segment: Mechatronics, Services & Others

- In our mechatronics business, demand for memory handlers and device interfaces with high correlation to memory testers is expected to increase.

- In our services, support and other businesses, the leasing / second-hand sales business will decline due to a shortage of second-hand testers and abundance of customers with ample funds; however, we anticipate that the growth of SSD testers can compensate for this.
Summary

✓ In 2018, Advantest aims for further business expansion amid growth in the tester market
✓ We will strive to continue to ensure a stable parts supply that tracks tester demand
✓ In June 2018, we globally integrated our semiconductor test equipment business. The new organization will enhance our competitiveness in promising areas such as AI, 5G comms and ADAS
✓ We are closely watching the risk of global economic stagnation due to Sino-American trade friction

○ Summary

• We have discussed the tester market outlook for 2018 and our future business prospects.
• Technology will not stagnate. In addition to 5G, many other events likely to expand the semiconductor tester market are anticipated in the future. Semiconductor applications are also expanding.
• Even if the tester market should hit a stagnation phase in the future, we have confidence that the adjustment will not be prolonged, and that Advantest will continue to grow as semiconductors become more complicated and sophisticated and the semiconductor supply chain evolves.
• While our 1Q results were favorable, to capitalize on the above growth opportunities, we will continue to execute necessary measures with short-term, medium-term and long-term perspectives.
• However, we believe that it is also necessary to be aware of the rise of protectionism and trade friction between the US and China and other countries, and we will continue to be poised to react to rapid market fluctuations.
NOTE

◆ Accounting Standards
Our results and outlook, described in this presentation, have been prepared in accordance with IFRS.

◆ Cautionary Statement with Respect to Forward-Looking Statements
This presentation contains “forward-looking statements” that are based on Advantest’s current expectations, estimates and projections. These statements include, among other things, the discussion of Advantest’s business strategy, outlook and expectations as to market and business developments, production and capacity plans. Generally, these forward-looking statements can be identified by the use of forward-looking terminology such as “anticipate,” “believe,” “estimate,” “expect,” “intend,” “project,” “should” and similar expressions. Forward-looking statements are subject to known and unknown risks, uncertainties and other factors that may cause Advantest’s actual results, levels of activity, performance or achievements to be materially different from those expressed or implied by such forward-looking statements.

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