ADC Linearity Test Solution

EVA100 MEASUREMENT SYSTEM

Easy and High-accuracy Linearity Measurement

All-in-one measurement functions and software.

Intuitive application tools.

High-accuracy, high-speed measurement solutions.
**ADC Linearity Testing**
The EVA100 has a measurement module for ADC linearity testing that is optimized for easy and high-accuracy measurements.

Linearity measurement configuration

**17-bit Linear Ramp for High-Speed Measurement**
The EVA100 has a linear ramp high-speed measurement function for high-speed sampling of ADCs.
- 17-bit linear ramp signal accuracy
- 14-bit ADC testing

**Synchronized Measurement Settings**
The intuitive GUI makes it easy to set measurement timing and synchronize the hardware, enabling highly repeatable measurements.

**Step ramp waveform setting**
Setting items
- START voltage
- STOP voltage
- STEP voltage
- Time interval

**AD sampling timing and data output timing**
Display step ramp waveform and AD sampling timing on the same GUI for easy-to-check synchronized settings.

Set AD sampling timing and data output timing by timing chart.

**Test Sequences Set by Template**
A standard template makes it easy to set measurement conditions.

**Specifications**
- Stepping ramp linearity: 15-bit (typ.)
- Linear ramp linearity: 17-bit (typ.)
- Linear ramp output period: 500 us ~ 500 ms
- Histogram calculation: High speed
- Hardware processing

---

**ADVANTEEST CORPORATION**
www.advantest.com

© 2015 ADVANTEST CORPORATION

EVA Project  E-mail: info_eva@advantest.com