

FY2017 3Q Information Meeting

January 30th, 2018
Advantest Corporation

NOTE

- ◆ Accounting Standards
 - Our results and outlook, described in this presentation, have been prepared in accordance with IFRS.
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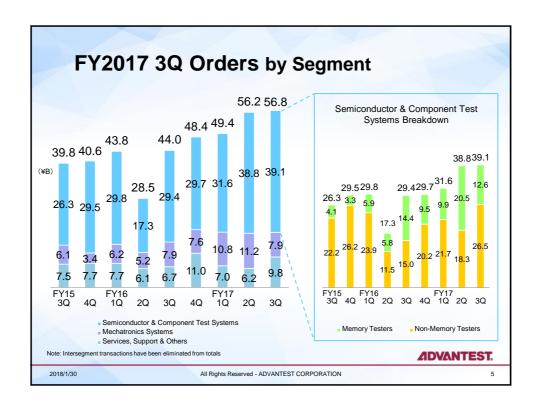
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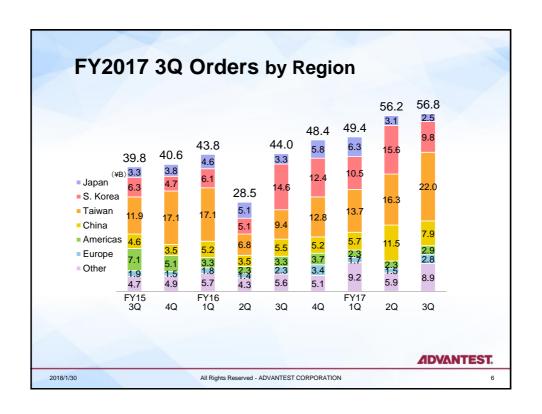




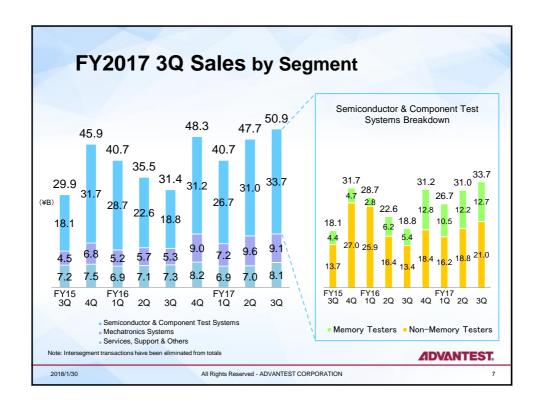
			FY2	016	16 FY2017				QoQ			(¥B) YoY	
		1Q	2Q	3Q	4Q	1Q	2Q	3Q	(Δ)	(%)	(Δ)	(%)	
Orders		43.8	28.5	44.0	48.4	49.4	56.2	56.8	+0.6	+1.0%	+12.8	+29.0%	
Sales		40.7	35.5	31.4	48.3	40.7	47.7	50.9	+3.2	+6.6%	+19.5	+62.1%	
Gross Profit Gross Profit Margin		25.3 62.2%	21.0 59.3%	18.1 57.6%	25.3 52.4%	20.6 50.6%	24.9 52.1%	23.2 45.8%	-1.6 -6.3pts	-6.4%	+5.2 -11.8pts	+29.0%	
Operating Income Operating Income Margin		5.7 14.1%	2.6 7.3%	1.0 3.1%	4.6 9.5%	2.2 5.5%	5.2 10.9%	3.2 6.4%	-2.0 -4.5pts	-37.7%	+2.3 +3.3pts	+232.3%	
Income Before Tax		6.2	2.9	0.6	5.3	1.6	4.8	3.4	-1.4	-29.3%	+2.8	+438.7%	
Net Income Net Income Margin		5.0 12.2%	2.3 6.4%	0.1 0.4%	6.8 14.1%	1.0 2.4%	3.9 8.2%	2.6 5.0%	-1.4 -3.2pts	-35.3%	+2.4 +4.6pts	x22.7	
Backlog		36.5	29.5	42.2	42.2	50.9	59.4	65.3	+5.9	+9.9%	+23.1	+54.9%	
Exchange Rate	1 US\$	¥111 ¥124	¥104 ¥116	¥105 ¥115	¥115 ¥122	¥112 ¥121	¥111 ¥128	¥112 ¥132		reciation		reciation	

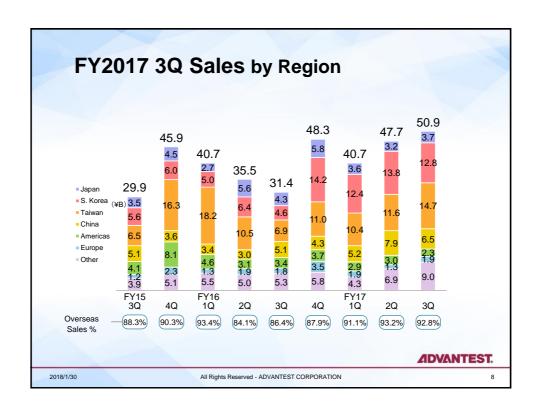




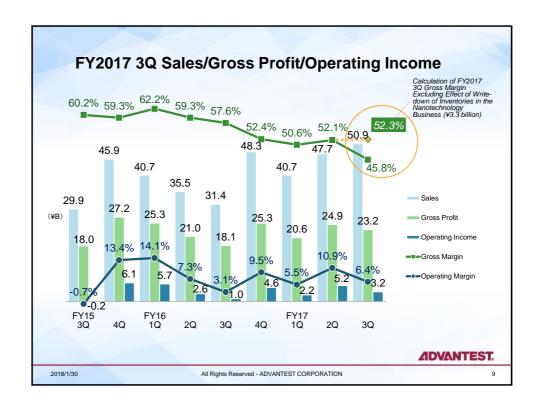


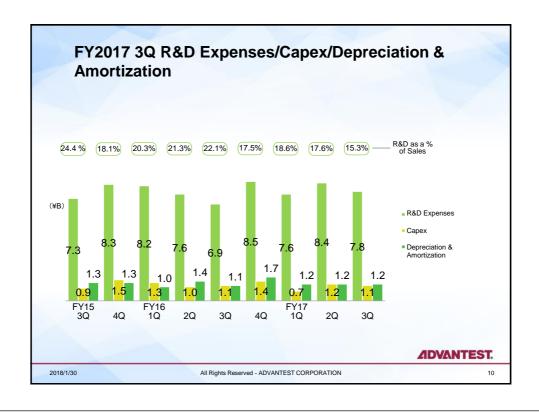




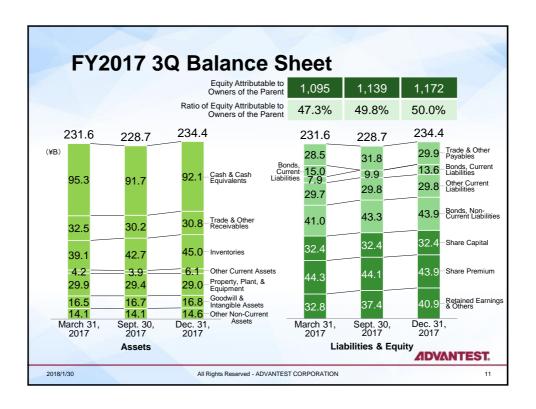


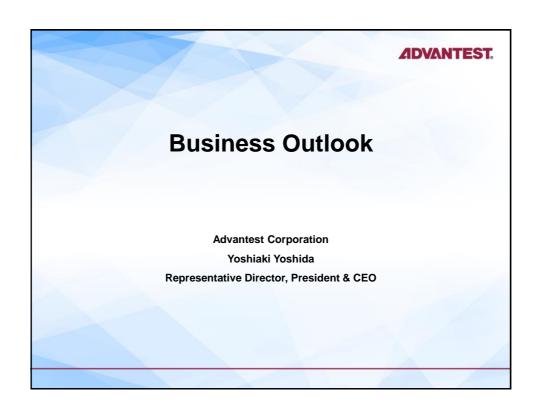














Market Forecast for CY2018

	CY17 estimate	CY18 estimate	YoY change
Non-memory tester market	ca. \$2,200M	ca. \$2,200~2,300M	ca. +0% to +5%
Memory tester market	ca. \$750M	ca. \$850∼900M	ca. +10% to +20%

- ✓ We expect the brisk pace of the semiconductor tester market to continue in 2018
- ✓ In addition to steady non-memory tester demand from the automotive and display-related sectors, chip-makers will increase their investments in miniaturization and functionality improvements of logic ICs
- ✓ Demand for memory testers is also expected to continue at a high level

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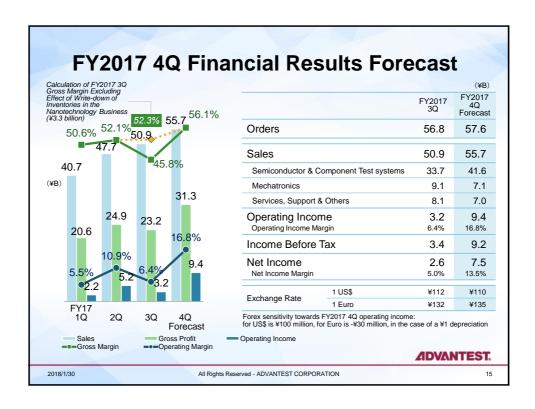
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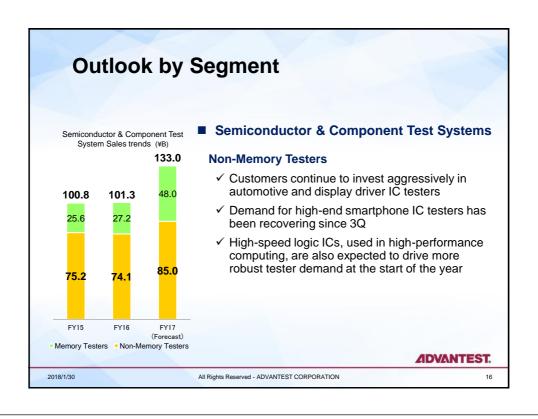
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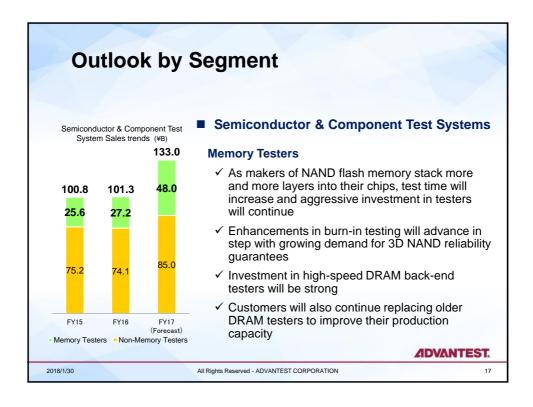
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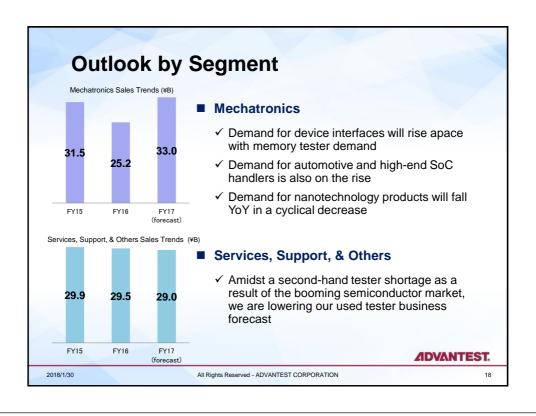
	inancia				Just	(¥B)
	FY20)16	FY2017 Forecast (October)	FY2017 Forecast (January)	vs. F (Δ)	Y2016 (%)
Orders	16	4.7	188.0	220.0	+55.3	+33.6%
Sales		5.9	180.0	195.0	+39.1	+25.1%
Semiconductor & Component Tes	t systems 10	1.3	118.5	133.0	+31.7	+31.3%
Mechatronics	2	25.2	30.5	33.0	+7.8	+31.0%
Services, Support & Others	2	9.5	31.0	29.0	-0.5	-1.7%
Operating Income Operating Income Margin	= 1	3.9 ^{3.9%}	18.0 10.0%	20.0 10.3%	+6.1 +1.4pts	+43.8%
Income Before Tax		5.0	17.0	19.0	+4.0	+26.5%
Net Income Net Income Margin		4.2).1%	14.5 8.1%	15.0 7.7%	+0.8 -1.4pts	+5.6%
Backlog	4:	2.2	50.2	67.2	+25.0	+59.2%
Annual Dividend Per Share		¥25	¥26	¥26	+¥1	
1 US\$	¥	€108	¥110	¥111	¥3 Depreciation	
Exchange Rate 1 Euro	¥	£119	¥130	¥129	¥10 De	preciation











Key Measures

Preparing for business expansion

<Pre><Preparation for sustained growth>

Reinforcing development and support for high-potential applications such as AI, 5G communication, and ADAS

<Increasing production capacity>

Expanding production capacity to keep up with the pace of market growth

Outlook for investments in FY17: Unchanged

✓ R&D expenses:
 ¥34.0B (FY2016 actual: ¥31.2B)
 ✓ Capex:
 ¥ 6.5B (FY2016 actual: ¥4.8B)
 ✓ Depreciation and amortization:
 ¥ 5.0B (FY2016 actual: ¥5.2B)

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Summary

- ✓ We expect brisk demand in the semiconductor tester market to continue in 2018
- ✓ The non-memory tester market is taking a favorable turn at present. Going forward, we expect overall high levels of demand for logic, automotive, and display driver ICs
- ✓ The memory tester market will similarly benefit from strong demand for non-volatile memory and DRAM test
- We are doing our utmost to increase our production capacity amid greater-than-expected increases in demand
- We will move forward on a continuing basis with foundational enhancements to address high-potential applications such as AI, 5G communication, and ADAS

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