### **ADVANTEST**®

# FY2018 (Period Ended March 31th, 2019) Information Meeting

April 25th, 2019 Advantest Corporation



### NOTE

#### **Accounting Standards**

-Our results and outlook, described in this presentation, have been prepared in accordance with IFRS.

#### Cautionary Statement with Respect to Forward-Looking Statements

- This presentation contains "forward-looking statements" that are based on Advantest's current expectations, estimates and projections. These statements include, among other things, the discussion of Advantest's business strategy, outlook and expectations as to market and business developments, production and capacity plans. Generally, these forward-looking statements can be identified by the use of forward-looking terminology such as "anticipate," "believe," "estimate," "expect," "intend," "project," "should" and similar expressions. Forward-looking statements are subject to known and unknown risks, uncertainties and other factors that may cause Advantest's actual results, levels of activity, performance or achievements to be materially different from those expressed or implied by such forward-looking statements.

#### Use of These Materials

- The information contained in this presentation is protected under intellectual property laws, such as copyright law, patent law, trademark law and design law, and other laws and statutes of each country and various treaties. Any use (modification, copying, diversion, etc.) of this information that goes beyond that which is clearly authorized by law and statutes, and is not approved in writing by our company in advance, is forbidden.

All Rights Reserved - ADVANTEST CORPORATION

### **Financial Results for FY2018**

Atsushi Fujita
Managing Executive Officer (CFO)



### **FY18 Results Highlights**

- In response to increased demand for higher semiconductor performance and enhanced reliability assurance, robust demand for testers continued
- Although the business environment deteriorated in the second half, with semiconductor inventory adjustments appearing, orders set a new record
- Our efforts to strengthen our production and support systems in order to meet high demand have enabled a new sales record
- As a result of measures to improve operational efficiency, net income, ROE and annual dividends also set new records

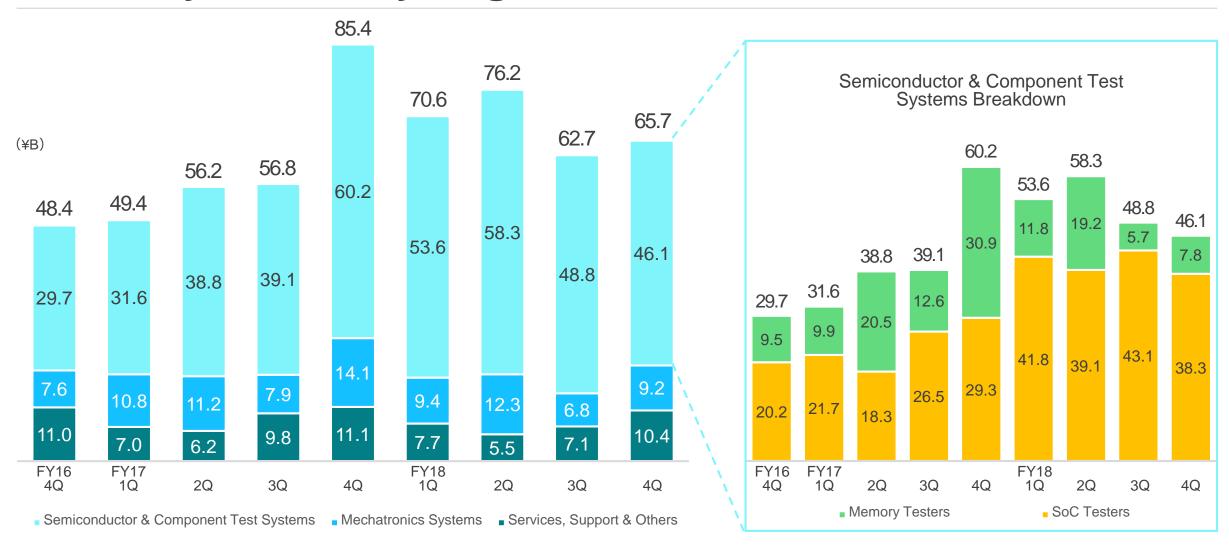
# **FY18 Summary of Results**

						(¥B)
		FY17	FY18			
		Results	Forecast as	Results	vs. F	/17
		Nesults	of Jan. 2019	Nesulis	(Δ)	(%)
Orders		247.8	265.0	275.2	+27.4	+11.1%
Sales		207.2	278.0	282.5	+75.2	+36.3%
Operating Income	9	24.5	63.0	64.7	+40.2	+2.6x
Operating Margin		11.8%	22.7%	22.9%	+11.1pts	
Income Before Tax		24.3	64.0	66.2	+41.9	+2.7x
Net Income		18.1	54.5	57.0	+38.9	+3.1x
Net Income Margir	ı	8.7%	19.6%	20.2%	+11.5pts	
IFRS15 Backlog adjust	ment		▲3.0	▲3.0		
Increase in backlog due of Astronics SLT busine	e to acquisition			+2.4		
Backlog	Backlog		66.8	74.9	<b>▲</b> 7.9	<b>▲</b> 9.5%
Exchange Rate —	1 US\$	¥111	¥110	¥110	¥1 Appre	ciation
	1 Euro	¥129	¥130	¥129	-	
Dividend per share		¥32	¥88	¥92	up ¥60	

# FY18 4Q Summary of Results

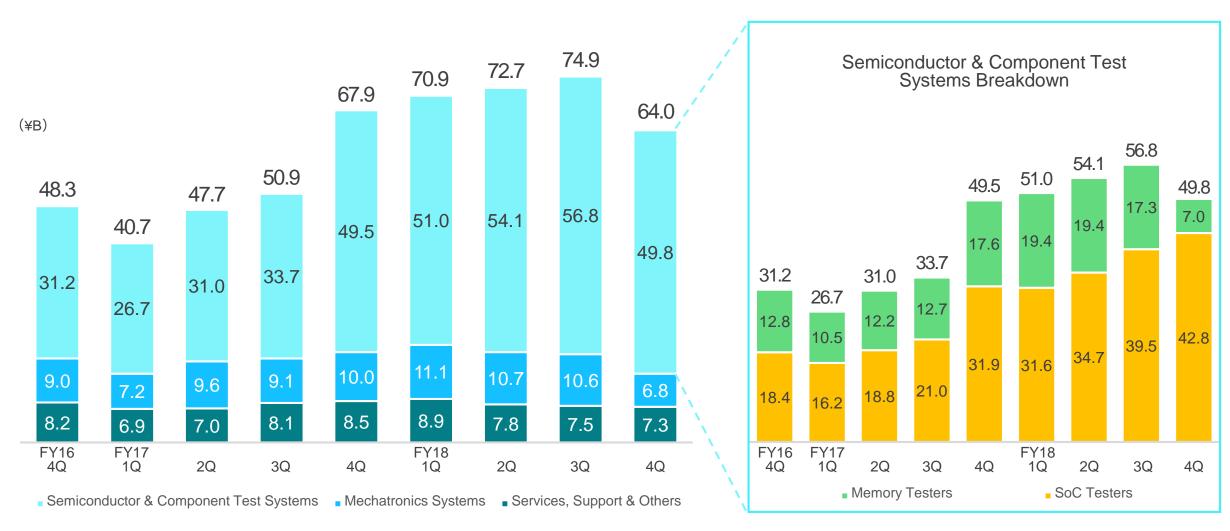
													(¥B)
			FY	17			FY18						
		1Q	2Q	3Q	4Q	1Q	2Q	3Q	4Q	Qo		Yo	
										(Δ)	(%)	(Δ)	(%)
Orders		49.4	56.2	56.8	85.4	70.6	76.2	62.7	65.7	+2.9	+4.7%	<b>▲</b> 19.8	▲23.1%
Sales		40.7	47.7	50.9	67.9	70.9	72.7	74.9	64.0	<b>▲</b> 11.0	<b>▲</b> 14.6%	<b>▲</b> 4.0	<b>▲</b> 5.9%
Gross Profit		20.6	24.9	23.2	37.9	38.2	38.9	42.5	34.4	▲8.1	<b>▲</b> 19.1%	▲3.4	▲9.1%
Gross Margin		50.6%	52.1%	45.8%	55.7%	53.9%	53.6%	56.8%	53.8%	▲3.0pts		▲1.9pts	
Operating Income	9	2.2	5.2	3.2	13.9	15.8	18.0	20.7	10.2	<b>▲</b> 10.5	<b>▲</b> 50.8%	<b>▲</b> 3.6	<b>▲</b> 26.3%
Operating Margin		5.5%	10.9%	6.4%	20.4%	22.3%	24.7%	27.6%	15.9%	▲11.7pts		▲4.5pts	
Income Before Ta	ax	1.6	4.8	3.4	14.5	16.5	18.6	20.6	10.5	<b>▲</b> 10.1	<b>▲</b> 49.1%	<b>▲</b> 4.0	<b>▲</b> 27.7%
Net Income		1.0	3.9	2.6	10.6	13.9	16.2	17.9	9.0	▲9.0	▲50.1%	<b>▲</b> 1.7	<b>▲</b> 15.9%
Net Income Margir	n	2.4%	8.2%	5.0%	15.6%	19.6%	22.4%	23.9%	14.0%	▲9.9pts		▲1.6pts	
IFRS15 Backlog adjust	ment					▲3.0							
Increase in backlog due t of Astronics SLT busines	o acquisition s								+2.4				
Backlog		50.9	59.4	65.3	82.8	79.5	83.0	70.8	74.9	4.1	+5.8%	<b>▲</b> 7.9	<b>▲</b> 9.5%
Exchange Rate	1 US\$	¥112	¥111	¥112	¥111	¥108	¥111	¥113	¥111	¥2 Appr	eciation	-	
LAGRANGE IVALE	1 Euro	¥121	¥128	¥132	¥134	¥131	¥129	¥130	¥126	¥4 Appr	eciation	¥8 Appr	eciation

### **Quarterly Orders by Segment**



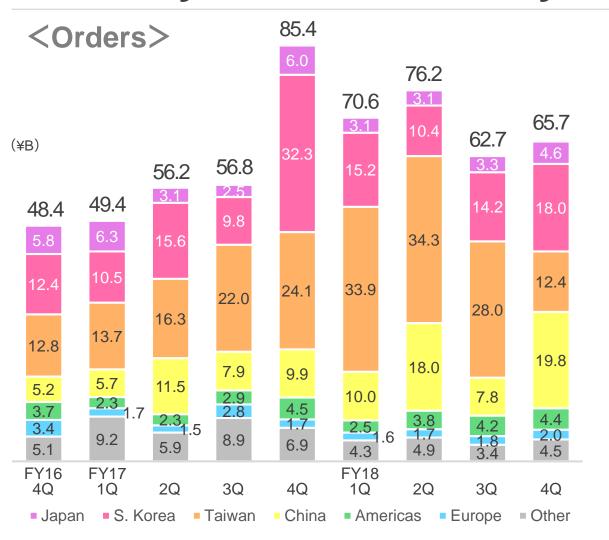
Note: Intersegment transactions have been eliminated from totals

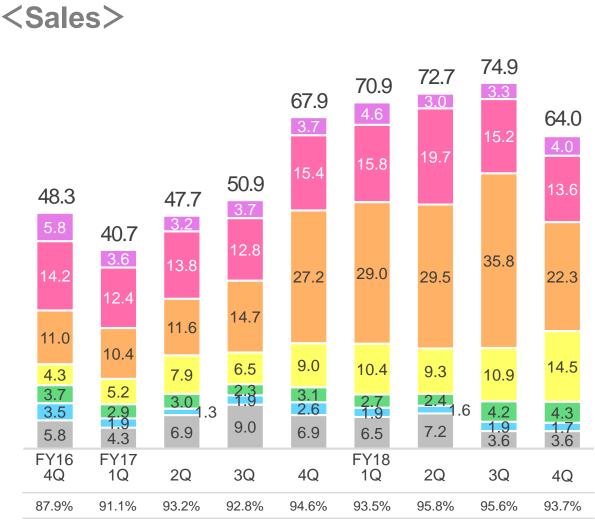
### **Quarterly Sales by Segment**



Note: Intersegment transactions have been eliminated from totals

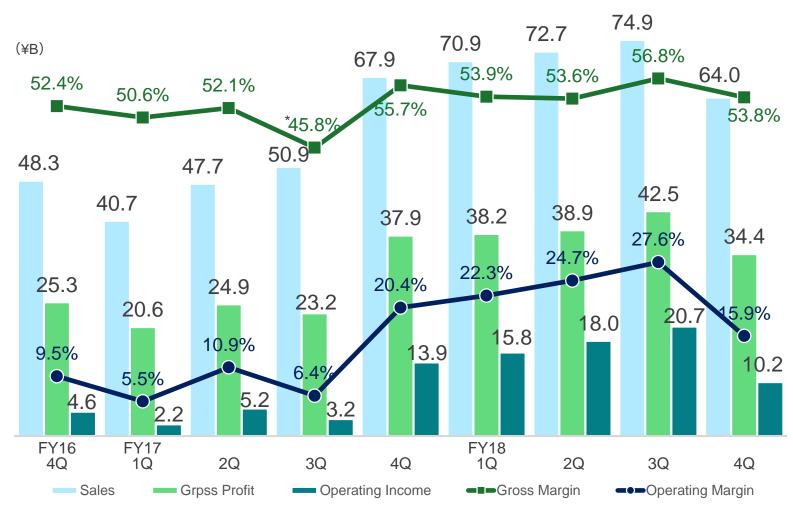
### **Quarterly Orders / Sales by Region**





Overseas Sales %

### Sales / Gross Profit / Operating Income



		( /
	FY17	FY18
Sales	207.2	282.5
Gross Profit Gross Margin	106.6 51.4%	154.0 54.5%
SG&A	82.1	89.3
Operating Income Operating Margin	24.5 11.8%	64.7 22.9%

(¥B)

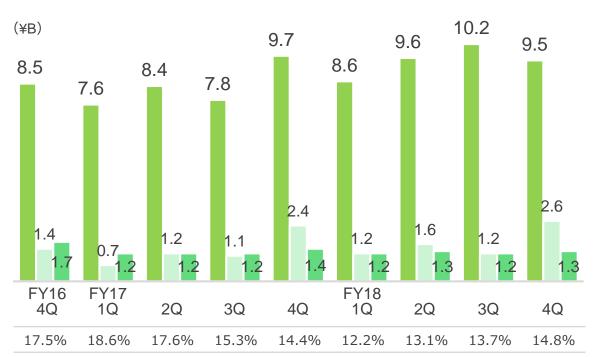
\*In FY17 3Q, a ¥3.3 billion write-down of inventories was recorded in our nanotechnology business. The gross margin in FY17 3Q excluding the effect of the write-down was 52.3%.

### Investments / Cash Flow Condition

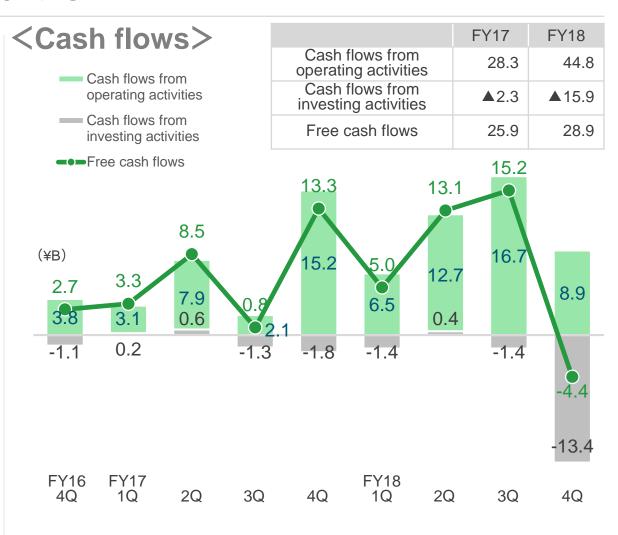
### <Investments>

- R&D Expenses
- Capital Expenditure
- Depreciation & Amortization

	FY17	FY18
R&D Expenses R&D as a % of Sales	33.5 16.2%	37.9 13.4%
Capex	5.4	6.6
D&A	5.0	5.0

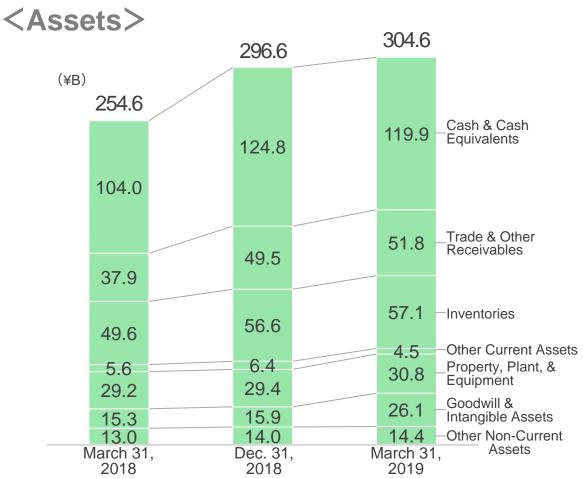


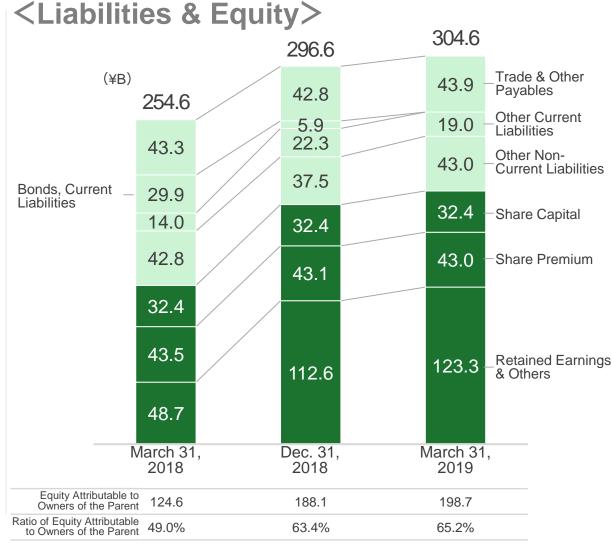
R&D as a % of Sales



<sup>\*</sup>Free Cash Flows = Cash flows from operating activities + Cash flows from investing activities

### **Financial Position**





# FY2019 Outlook & Mid-Term Business Plan Progress Report

Yoshiaki Yoshida Representative Director, President & CEO



### Tester Market Forecast (as of April 2019)

#### **CY18 Results**

- Amidst advancing semiconductor performance and reliability, many semiconductor manufacturers maintained active tester investment all year long
- -Based on market trends, CY18 market size was greater than our January estimates

#### **CY19 Forecast**

- As uncertainty gathers over the global economy, the tester market is also entering a slowdown phase, but is expected to improve in the second half of FY19
- -The CY20 tester market will expand again, driven by investment in next-generation advanced semiconductor test capabilities

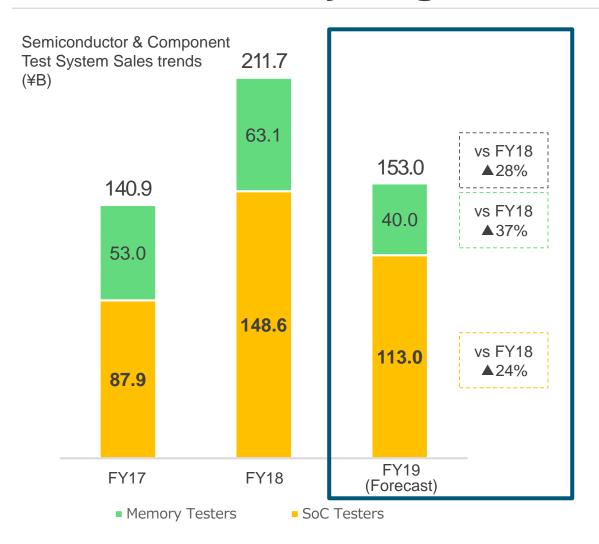
	CY18 Actual	CY19 Estimates	% Change
SoC Tester Market	Approx. \$2,550M (January estimate: approx. \$2.4B)	Approx. \$2,000M (Unchanged from January estimate)	Down approx. 20% YoY
Memory Tester Market	Approx. \$1,150M (January estimate: approx. \$1.0B)	Approx. \$550-650M (January estimate: approx. \$700-800M)	Down approx. 40-50% YoY

### **FY19 Forecast**

					(¥B
		FY18		FY19	
		Results	Forecast	vs. FY (Δ)	(%)
Orders		275.2	230.0	<b>▲</b> 45.2	<b>▲</b> 16.4%
Sales	Sales		230.0	<b>▲</b> 52.5	<b>▲</b> 18.6%
Operating Income		64.7	30.0	▲34.7	<b>▲</b> 53.6%
Operating Margin		22.9%	13.0%	▲9.9pts	
Income Before Tax	ncome Before Tax		31.0	▲35.2	▲53.2%
Net Income	Net Income		26.0	▲31.0	<b>▲</b> 54.4%
Net Income Margin		20.2%	11.3%	▲8.9pts	
Backlog		74.9	74.9	-	-
Exchange Rate*	1 US\$	¥110	¥110	-	
Lacriange Nate	1 Euro	¥129	¥130	¥1 Depred	ciation
ROE	ROE		12.6%	▲22.7	'pts

<sup>\*</sup>The latest outlook for the impact of exchange rate fluctuations on FY19 operating income: plus 500 million yen for each 1 yen of yen depreciation vs USD, and minus 100 million yen for each 1 yen of yen depreciation vs the euro.

### **FY19 Outlook by Segment**

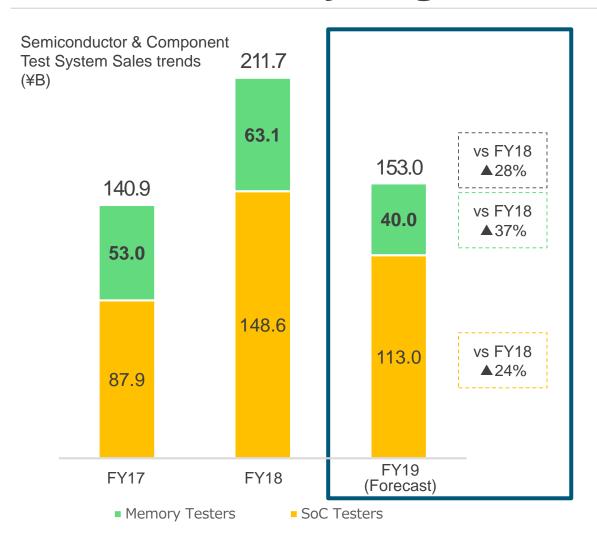


### **Semiconductor & Component Test Systems**

#### <SoC Testers>

- Expected to decline vs favorable FY18, but advancing semiconductor performance will support SoC tester demand
- The development of test capacity for semiconductors supporting higher-performance smartphones, such as application processors and image sensors, continues to increase
- The full-scale launch of EUV will also support demand for high-end processor testers
- Meaningful 5G-related demand is expected in CY20 and thereafter

### FY19 Outlook by Segment

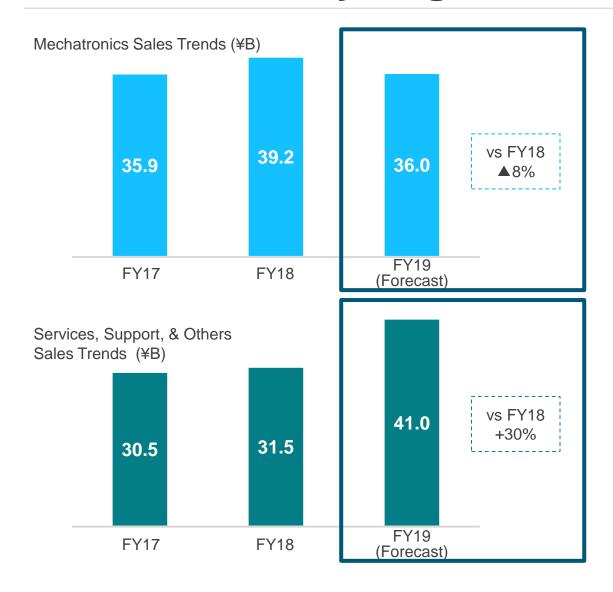


### **Semiconductor & Component Test Systems**

#### <Memory Testers>

- -Memory tester demand is being impacted by memory inventory adjustments
- -The timing of tester demand recovery depends on the pace of bit demand recovery, but is expected to come in 2H
- -In particular, we anticipate a positive trend in DRAM testers related to miniaturization, the shift to DDR5, and demand for high-speed products for HPC
- –NVM is expected to be primarily driven by demand related to multi-layer NAND investment

### **FY19 Outlook by Segment**



#### **Mechatronics Systems**

- –With the slowdown in investment by memory customers, DI products are expected to decline.
- EUV-related demand is expected to boost sales of nanotech products

### Services, Support, & Others

- -Customer tester utilization is steady, driving expectations of steady trends in field service income, the core business of this segment
- Segment revenue will increase due to our acquisition of the Astronics SLT business

### Mid/Long-Term Management Policy

#### **Grand Design** (Long-Term Management Policy: FY18-27)

#### <Long-Term Management Goals>

¥300B - ¥400B in annual sales

- Improve market share by 1% a year by focusing on growth areas and strengthening partnerships with customers (CY17 results: 36% → CY27 target: 46%)
- Develop new business around our core business area

#### <Cost / Profit Structure>

Balancing sales growth with cost efficiency

	Cost structure model @ ¥300B
Sales	¥300.0B
Cost of Sales	46%
SG&A as % of Sales	32%
(of which R&D expenses)	13%
Operating Margin	22%

#### **Mid-Term Management Plan** (FY18-20)

- Product demand inevitably fluctuates on year, so numerical targets are three-year averages
- Expand our sales and market share by providing outstanding solutions for the next generation of test needs.
- Aggressively invest in R&D and M&A for mid-to longterm sales growth
- While maintaining a consolidated payout ratio of 30%, based on our growth investment outlook, we will consider flexibility in our total shareholder return policy

	Mid-Term Plan (FY18-FY20 Average)			
	Conservative Base			
Yearly tester market growth ratio	0%	4%		
Sales	¥230.0B	¥250.0B		
Operating Margin	15%	17%		
ROE	15%	18%		
EPS	¥135	¥170		

### Initiatives for Mid/Long-Term Growth

#### **Important Measures Executed in FY18**

- -Focused on 5G and customer acquisition
- Realigned internal organization for long-term strategic execution
  - Globally integrated tester development teams
  - Created a team to identify areas for test business expansion, including the use of AI and data analytics
- Acquired the system-level test business of Astronics Inc. as a step toward expanding into adjacent business areas
- Introduced ROIC-based business management and evaluation tools to strengthen our business management system

ATS5034 System-level test system

#### **Important Measures for FY19**

- Enhance engagement with most important industry-leading customers
- -Maintain and expand market share
- Reinforce personnel and develop human resources for medium-to long-term growth
  - Reinforce R&D engineering staff and marketing / sales / SE / service staff in priority regions
- Improve business efficiency for operational excellence
- -Maximize utilization of ROIC in business operations

<R&D / Capital Investment Outlook>

	FY18 actual	FY19 forecast
R&D Expenses	37.9	37.0
Capex	6.6	10.0
D&A	5.0	6.0

### **Market Share Report**



<SoC Testers> CY18 Market Share: 52% (+22pts YoY)

< Memory Testers > CY18 Market Share: 58% (+1pt YoY)

### **5G Will Drive Mid/Long-Term Tester Demand**



- 5G is not only a mobile network technology, but a fundamental technology that may redefine our society
- Encourages the progress of the digital transformation in diverse areas; expected to create continuous demand for semiconductors and testers

### Relationship Between Early 5G & Tester Demand

**More & Better Comms Chips** 

**Better Smartphone Performance** 

**Data Center Growth** 

Early 5G will boost demand for comms semiconductor test to \$200M - 400M/year (Advantest estimate)

### 5G base station semiconductors

5G transmission distance/directional characteristics will increase volumes compared to 4G

## 5G baseband processors

Multi-mode, multi-band support & miniaturization will boost tester demand

#### 5G RF Semiconductors

Beamforming support will increase volumes compared to 4G

# Application processors

Further miniaturization & better performance will increase test items

#### DRAM

Miniaturization & adoption of LPDDR5 will boost tester demand

#### NAND flash

Larger capacities will increase test times

# Server processors

Further miniaturization & better performance will increase test items

#### Server DRAM

Higher speed/bandwidth devices such as DDR5, HBM, & GDDR will boost tester demand

#### Storage memory

Larger capacities & higher speeds will boost tester demand

5G accelerates the shift to higher performance and lower power consumption = test difficulty increases and test gets more complex

As new test challenges emerge, customers are making more support requests to Advantest

### Mid-Term Business Plan: 1<sup>st</sup> Fiscal Year Progress Report

	FY15-17		rm Plan 0 Average)		FY18 Results
	Average	Conservative	Base	l	Results
Sales	¥175.1B	¥230.0B	¥250.0B	 	¥282.5B
Operating Margin	9%	15%	17%		22.9%
ROE	12%	15%	18%		35.3%
EPS	¥74	¥135	¥1 <b>70</b>	 	¥ <b>302</b>

- Results exceeded the target for all KPIs in the base scenario of our mid-term management plan (FY18-20) in the first year of the plan, FY18, due to the expansion of the tester market and our significant share gains. We have made strong progress toward achieving our three-year goals
- The market trends that support our medium-to long-term growth, such as evolving semiconductor performance and the increasing importance of test, remain unchanged. While uncertainty in the global economy spreads to the tester market, we expect revenue to decline in FY19, but we expect results to recover in FY20
- We will maintain our mid-term numerical targets and continue to strive to achieve them